

Note: 10-1

8 Core DVFS FB setting (8 Core and 4 Core Co-Layout)

Schematic design notice of "10\_BB\_POWER" page.

Note 10-1: 4 mil GND trace with good shielding to PMIC (Differential)  
Differential pair of DVFS1 remote sense must be close to MT6752's ball.

Note 10-2: VCORE\_AO remote sense must be close to MT6752's ball.  
Remote sense trace with GNDshielding to PMIC (Differential)

Note 10-3: VCore\_PD remote sense must be close to MT6752's ball.  
Remote sense trace with GNDshielding to PMIC (Differential)

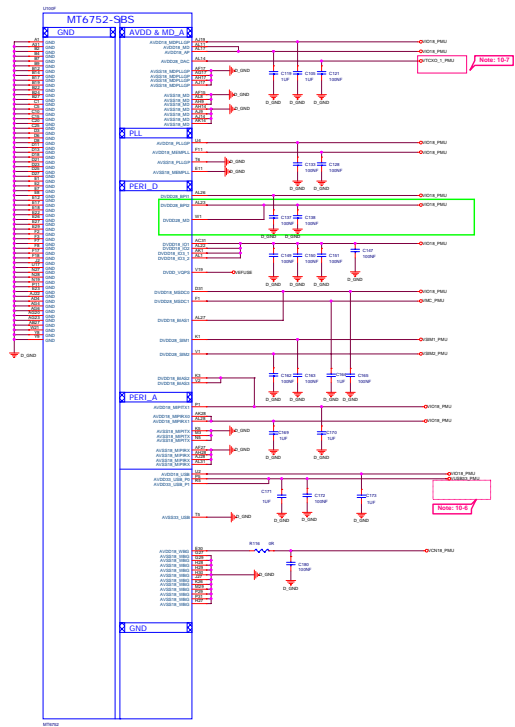
Note 10-4: VDRAM remote sense must be close to MT6752's ball.

Note 10-5: Differential pair of GPU remote sense must be close to MT6752's ball.

Note 10-6: Connect AVDD33\_USB\_P1 (R5 ball) to "VSIM1\_PMU" for IC-USB / Samrt card application.

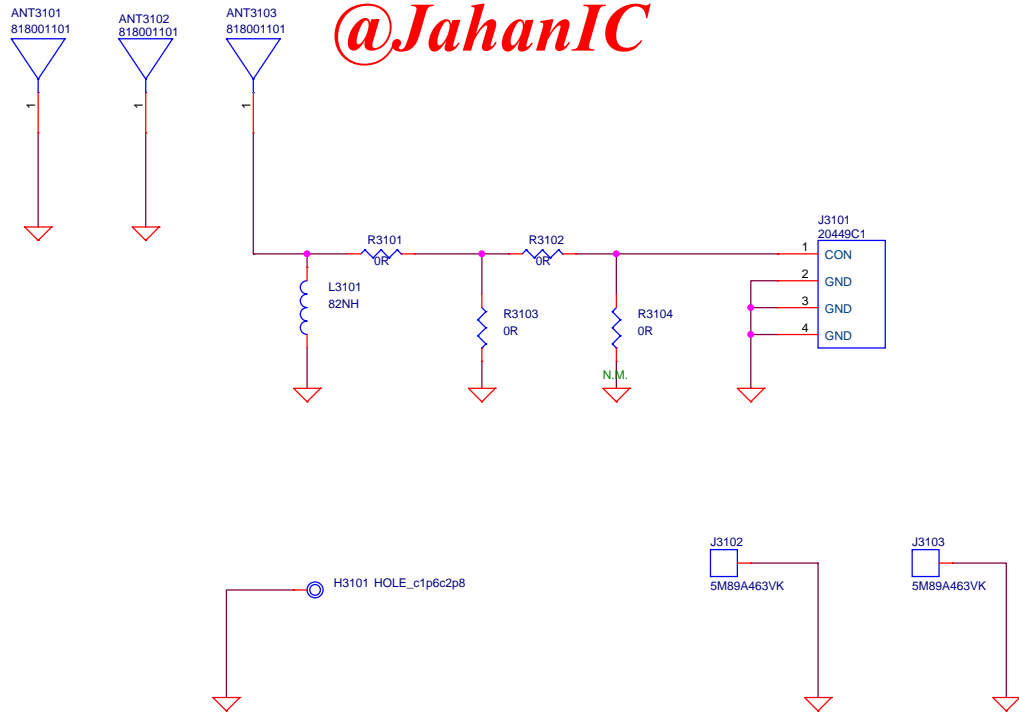
Connect AVDD33\_USB\_P1 (R5 ball) to  
"VUSB33\_PMU" for USB application.

Note 10-7: AVDD28\_DAC (AL14 ball) must be powered by "VTCXO\_1\_PMU". The de-coupling cap.has to be placed as close to BB as possible.



# Telegram Channel

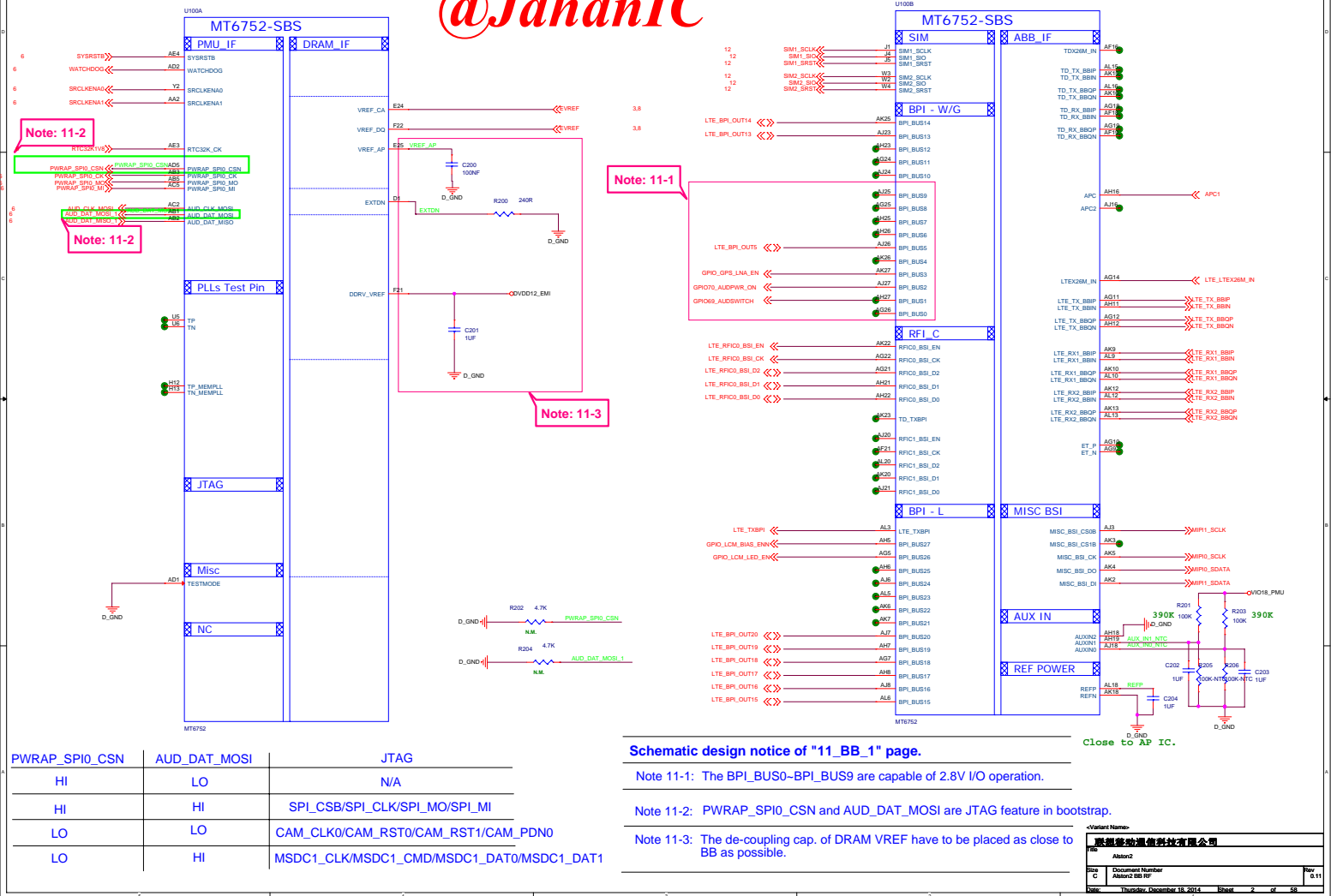
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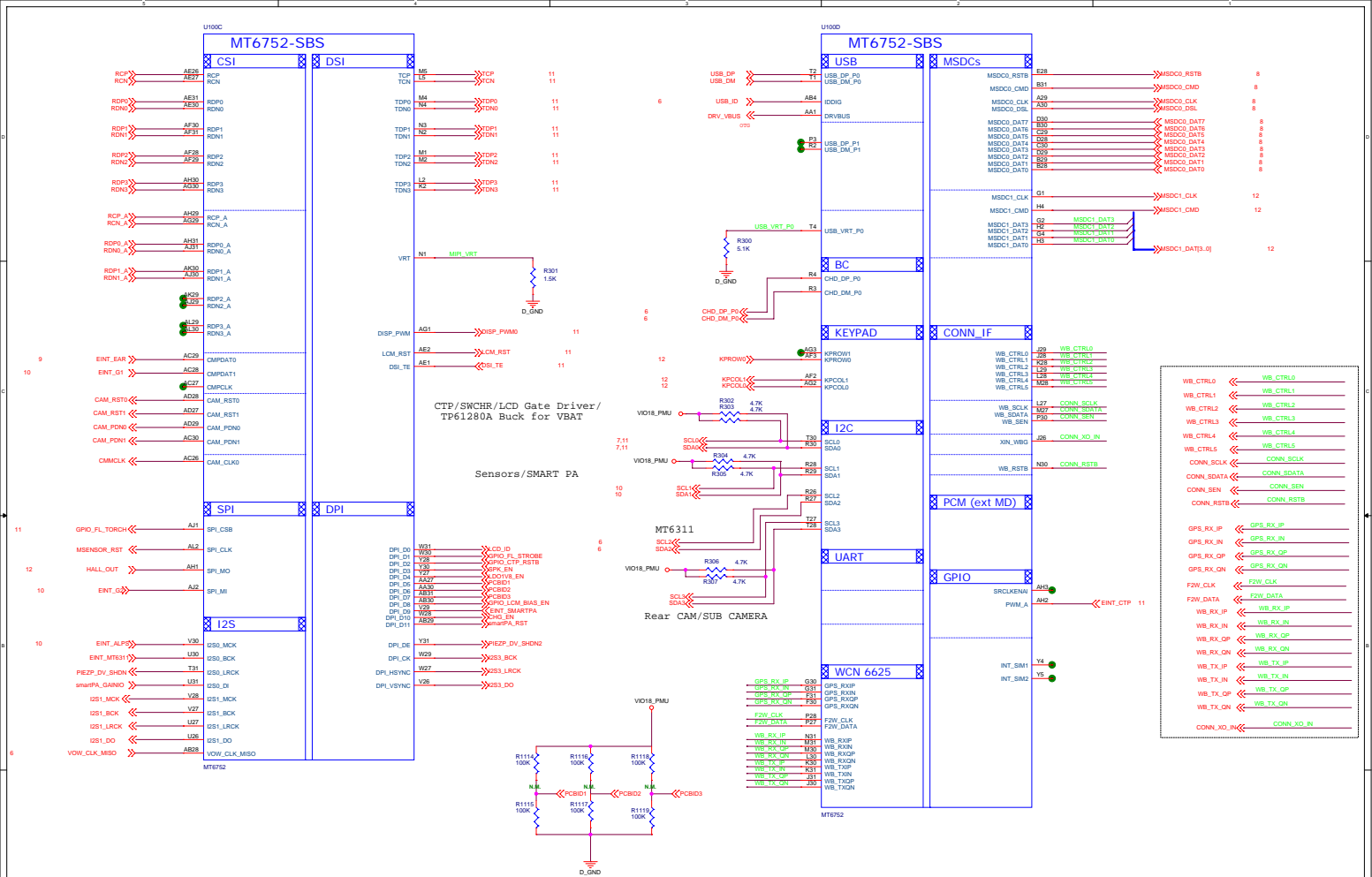


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# Telegram Channel

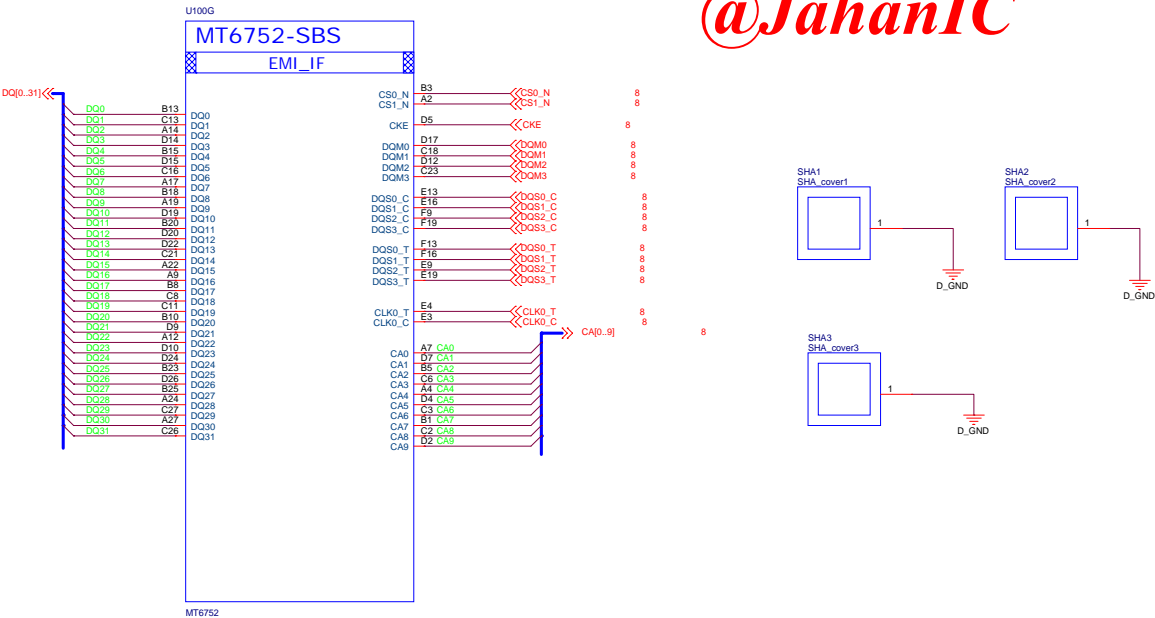
## @JahanIC





### Schematic design notice of "12\_BB\_2" page.

Note 12-1: I2C2 is dedicated for MT6311 2-phase buck control.

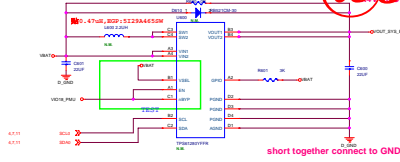


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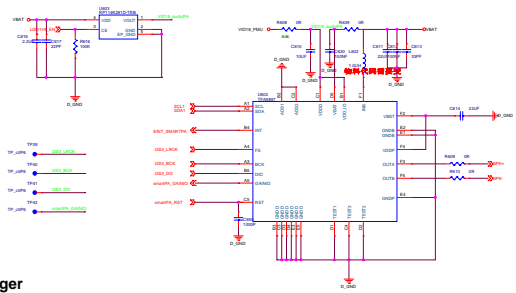


### System Boost for VBAT@LV

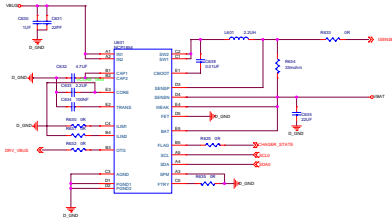
TPS61201A I2C address: 0x75 (Write:0x5A, Read:0x5B)



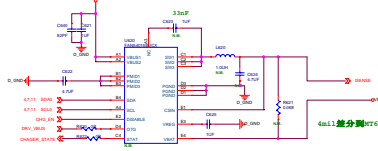
### SMART PA



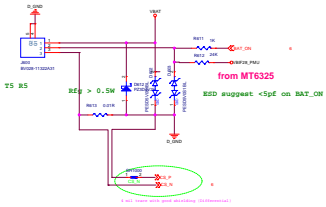
### 2A Switching Charger



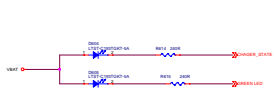
### 1A Switching Charger



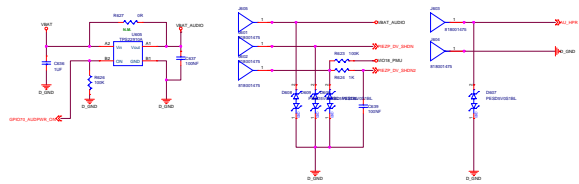
### BATTERY CONNECTOR



### LED LIGHT



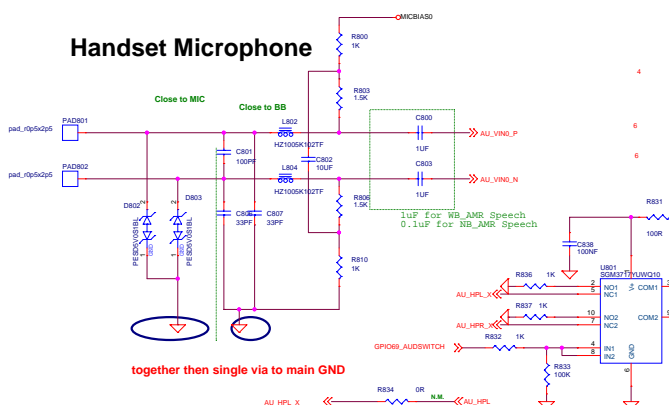
### PIEZO SPEAKER







A schematic diagram of a four-stage pump assembly. It consists of a horizontal shaft with four stages. The first stage on the left is a double-acting stage, represented by a diamond shape. The next two stages are single-acting stages, each represented by a rectangle with diagonal hatching. The final stage on the right is a double-acting stage, represented by a rectangle with horizontal lines. The entire assembly is labeled "L-R-G-M" below it.



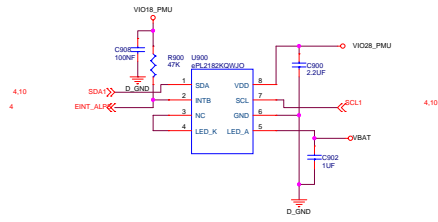
The schematic diagram illustrates the USB to RS-485 interface circuit. On the left, the USB side is connected to a USB symbol. A 1k5 resistor is connected between the USB signal line and the RS-485 signal line. A 100pF capacitor is connected between the USB signal line and ground. On the right, the RS-485 side is connected to a 1k5 resistor and a 100pF capacitor. The circuit is labeled "close to IC" and "close to connector".

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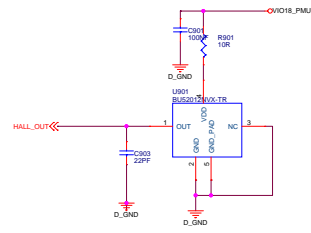
<b>&lt;Variant Name&gt;</b>					
<b>聚通移动通信科技有限公司</b>					
File Alston2					
Size C	Document Number Alston2 BB AUDIO IO				Rev 0.1
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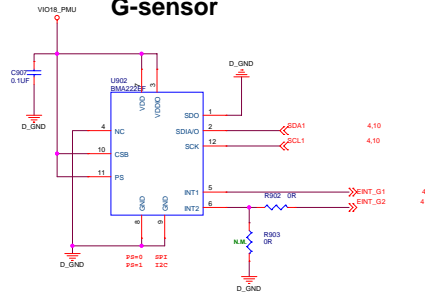
I2C address: 0X92,0X93



## HALL DEVICE

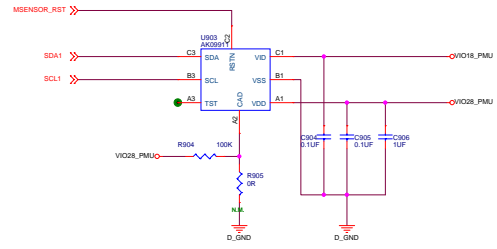


## G-sensor



```
I2C address:  (Write:0x30, Read:0x31)
```

### M-sensor

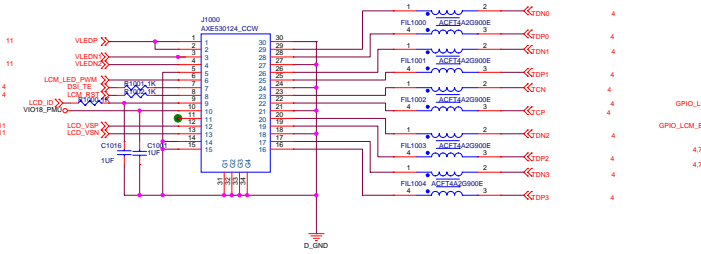


I2C address (AK09911: 0CH)

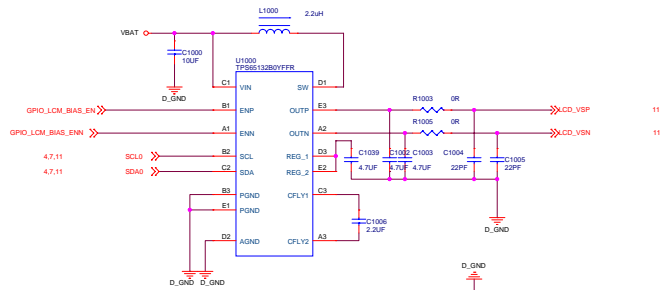
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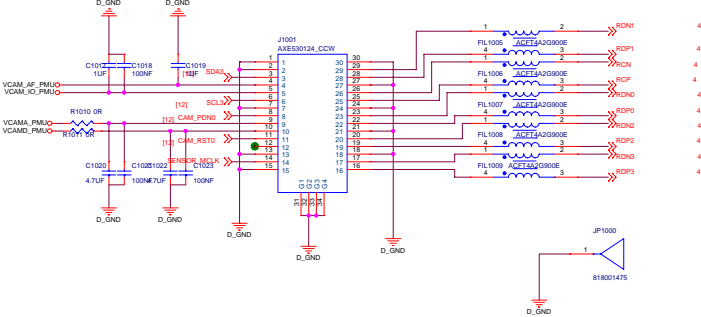
## Main LCM



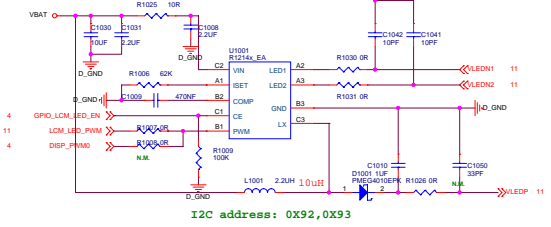
## LCD VSP&VSN



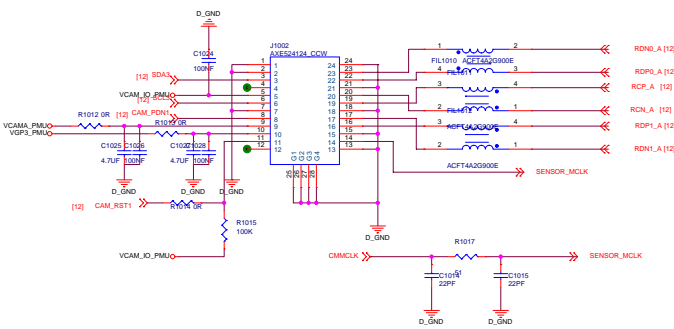
## REARE CAMERA



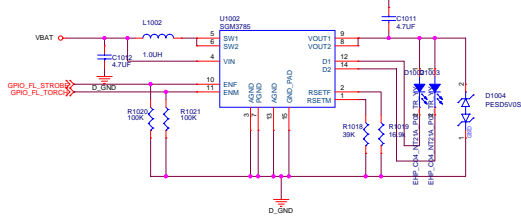
## LCD BACKLIGHT



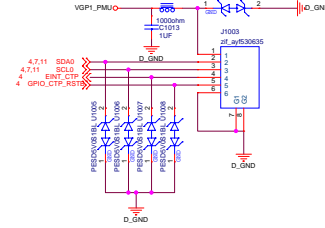
## SUB CAMERA



## Flash LED & DRIVER IC



## Touch Panel



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The schematic shows a 6x6 matrix keypad connected to a microcontroller. The keypad has pins labeled KEYPAD1 through KEYPAD36. The circuit includes a pull-up resistor network (R1100-R1105) connected to VCC and a pull-down resistor network (D1100-D1105) connected to D\_GND. A 100nF capacitor (C1102) is connected between D\_GND and the keypad's common ground pin. The keypad's output pins are connected to the microcontroller's I/O pins.

**Handset Microphone 2**

MC1 SOM013BL-G422-C1033-HS

Close to MIC

Close to BB

MIC\_BIAS0

AU\_VIN2\_P

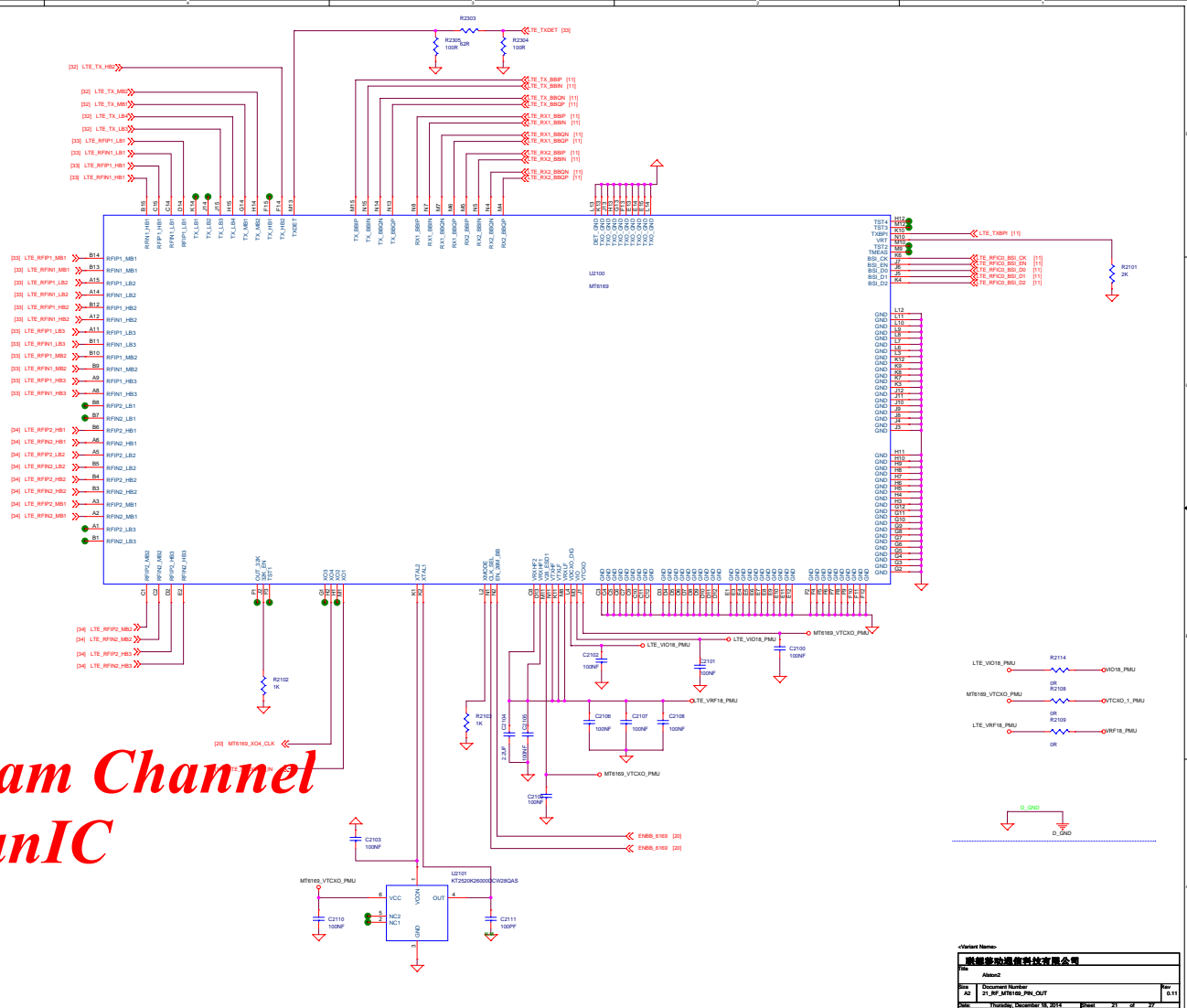
AU\_VIN2\_N

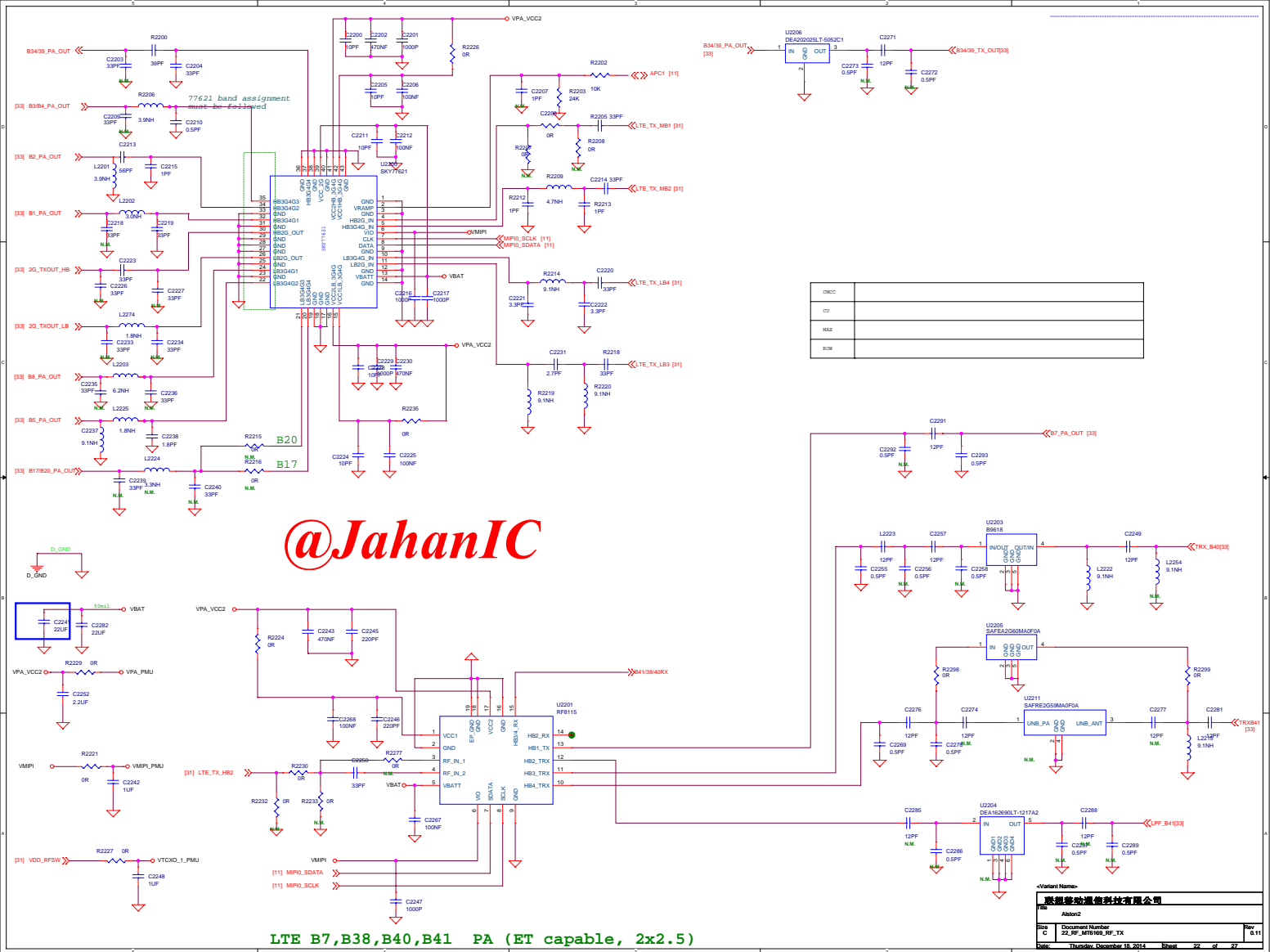
1uF for WB\_AMR Speech  
0.1uF for WB\_AMR Speech

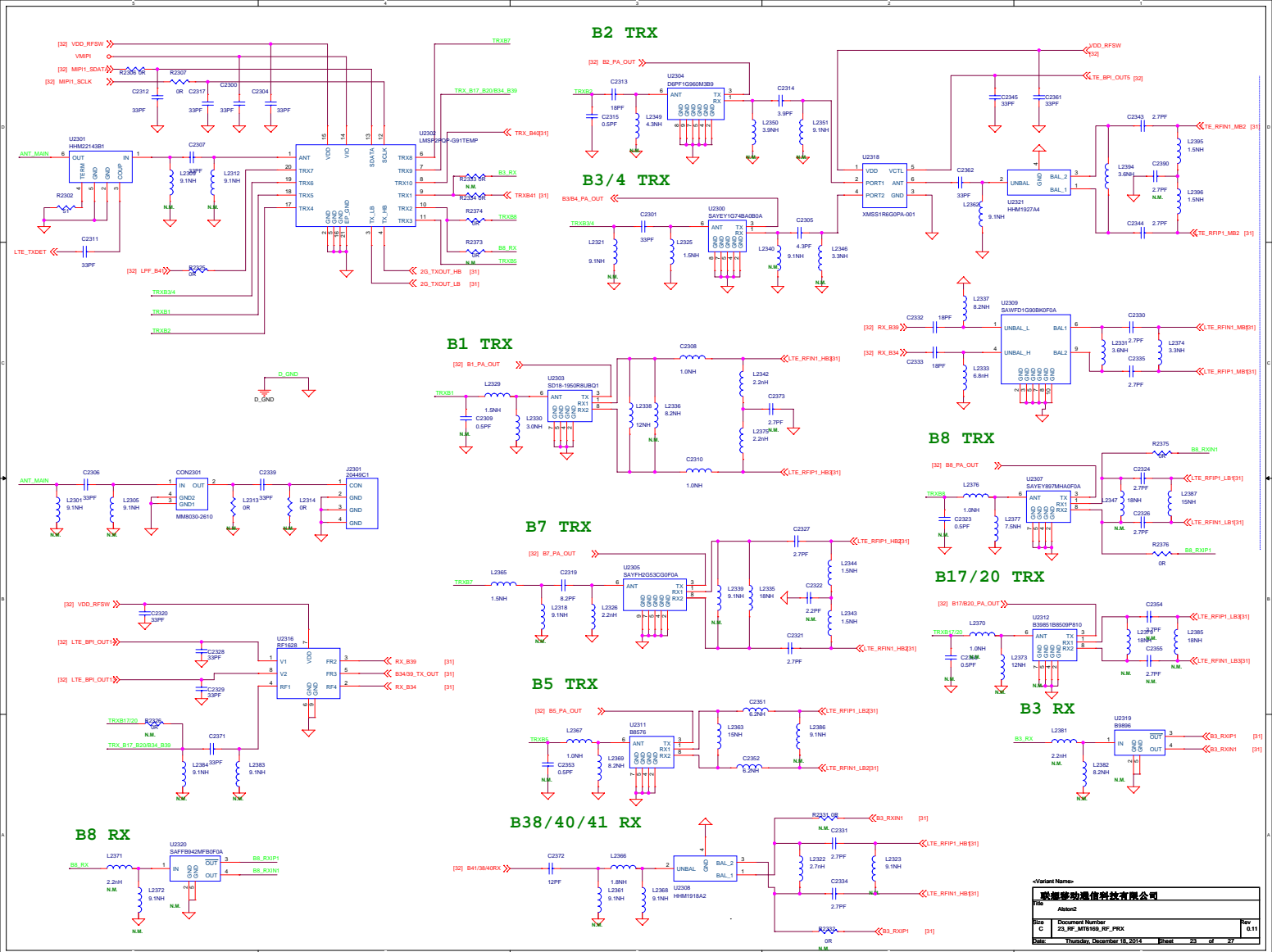
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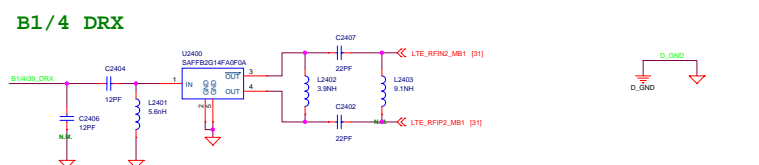
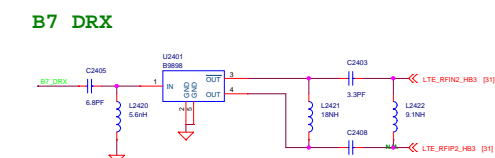












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